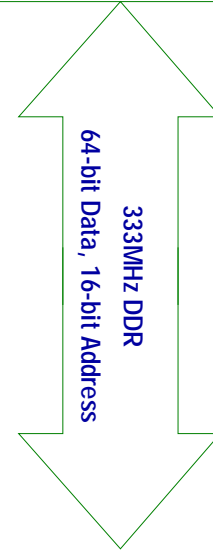


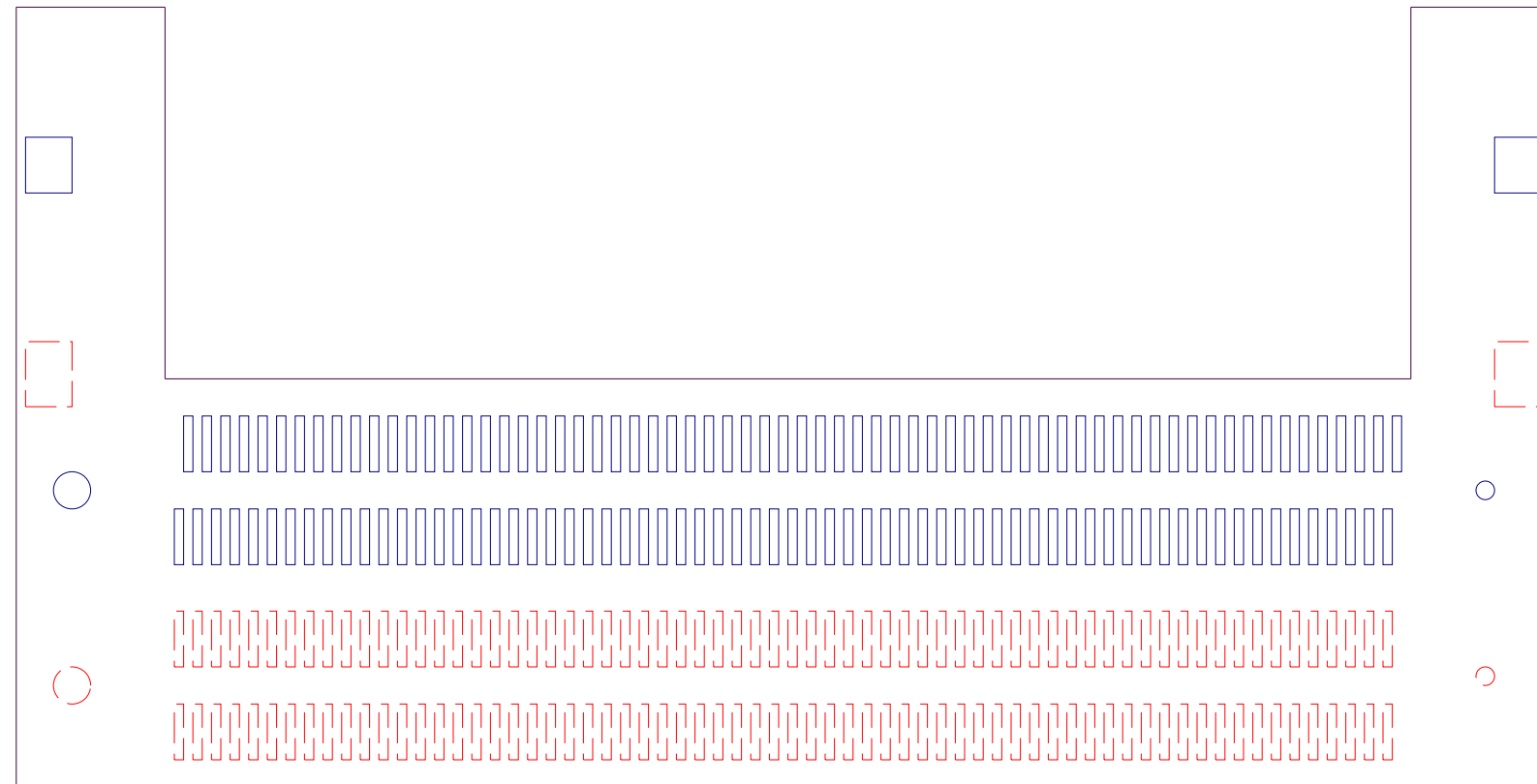
UC Davis ECE MSEE Thesis

DDR2 SDRAM SODIMM INTERPOSER BOARD

Micron
 DDR2 SDRAM
 SODIMM
 Top Side of Board
 MT16HTF25664H – 2GB



Micron
 DDR2 SDRAM
 SODIMM
 Bottom Side of Board
 MT16HTF25664H – 2GB



Blue = Top-Side (Correct Decal)
 Red = Bottom-Side (Wrong Decal)

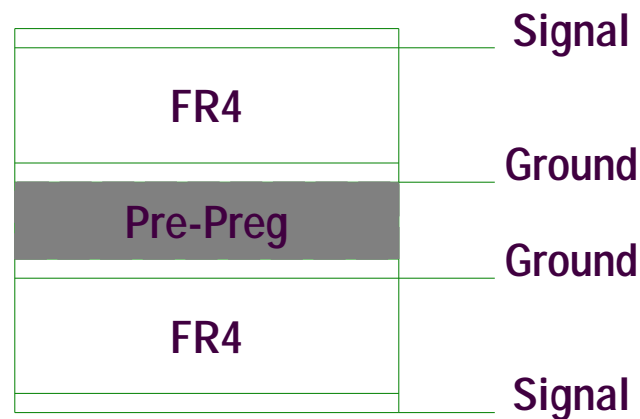


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File:	SODIMM_INTERPOSER		
Created by:	JEREMY W. WEBB	Date:	9-21-2009_8:29
Modified by:		Date:	
PCB NO:	389	Size:	B
	Sheet	1 of 4	REV: 3

A INDEX

SHEET/BLOCK	DESCRIPTION
1	TITLE PAGE/BLOCK DIAGRAM
2	SCHEMATIC DESIGN SHEET INDEX
3	DDR2 SDRAM SODIMM - MT16HTF25664H – 2GB (TOP - SIDE)
4	DDR2 SDRAM SODIMM - MT16HTF25664H – 2GB (BOTTOM - SIDE)

B Board Stack-Up

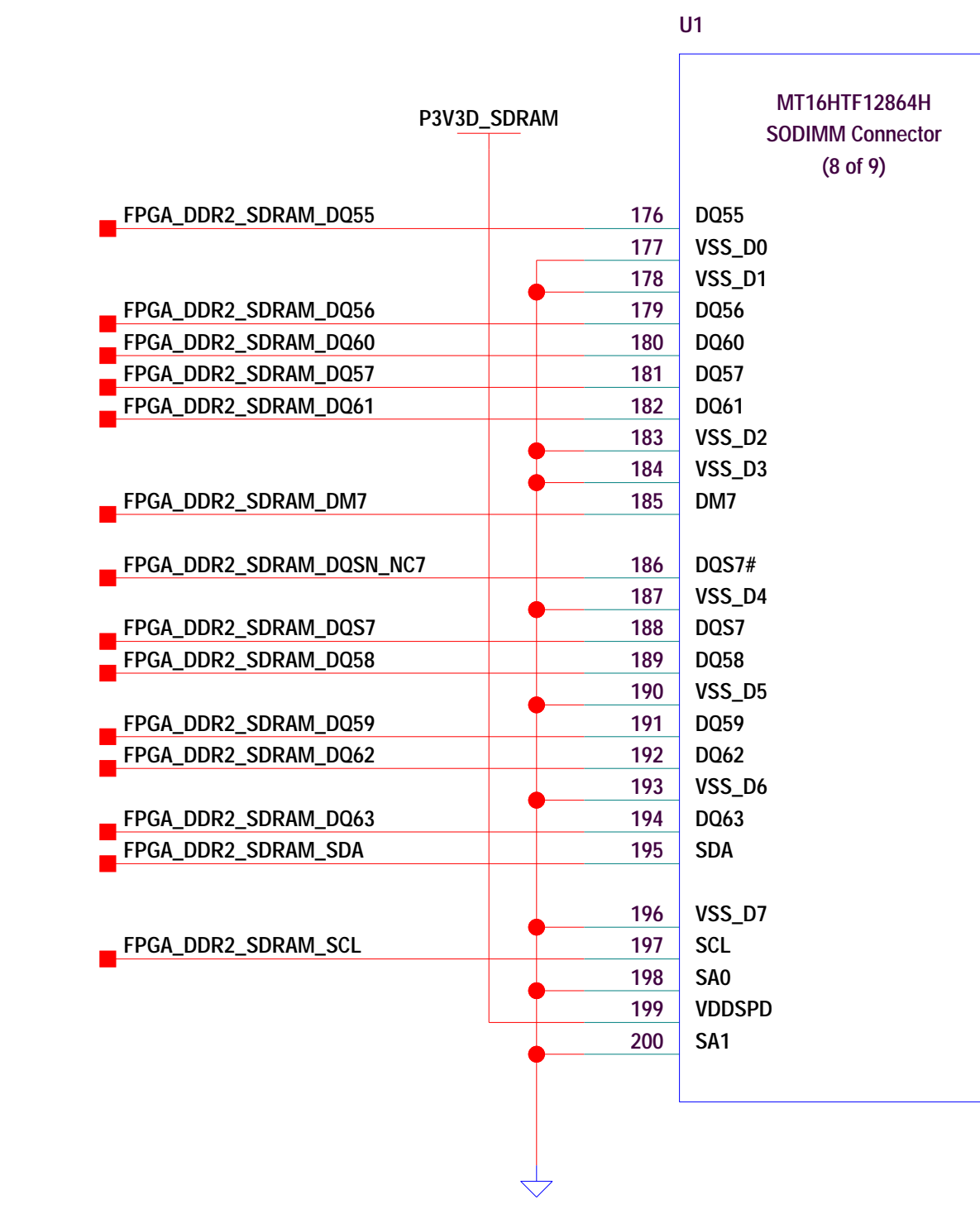
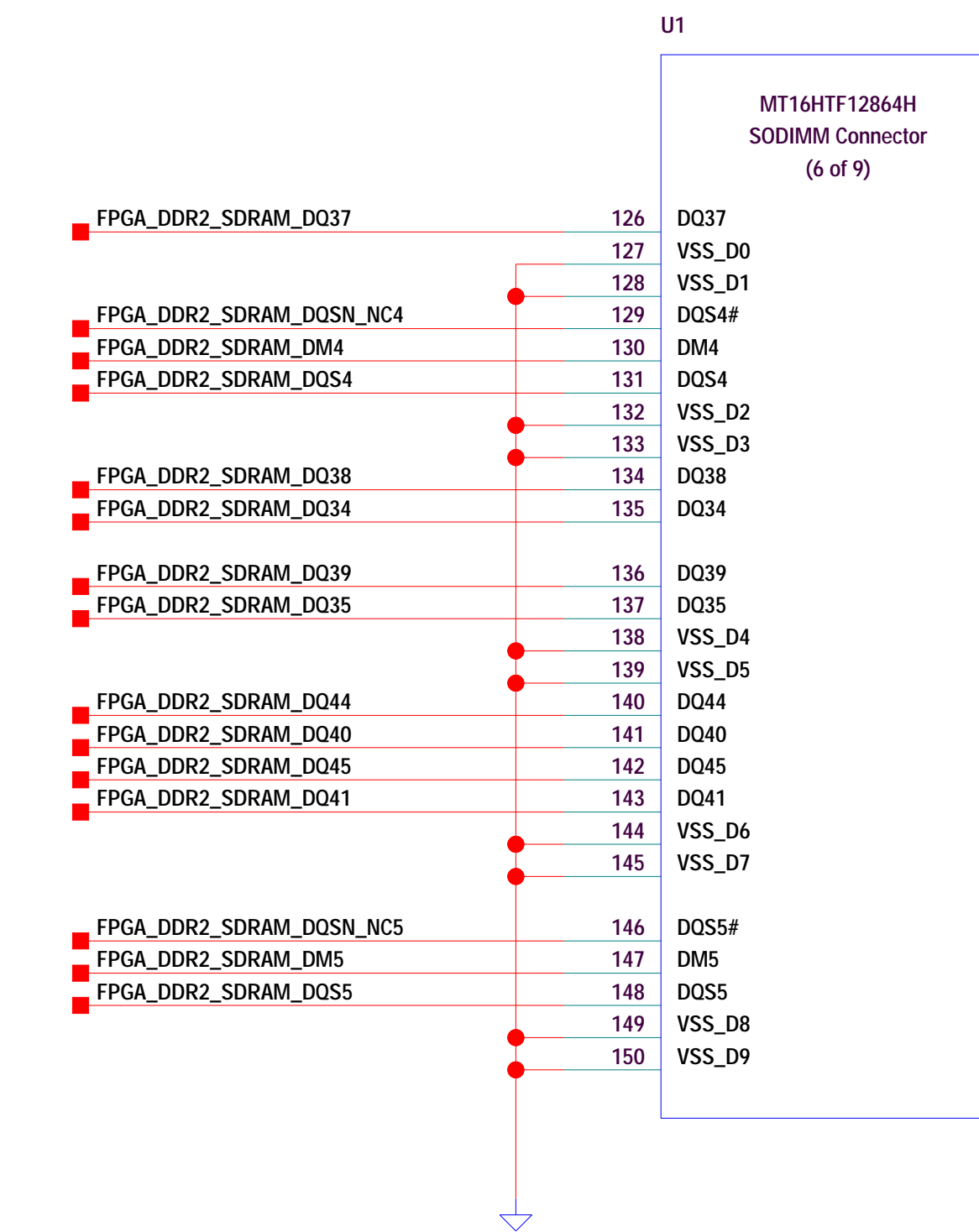
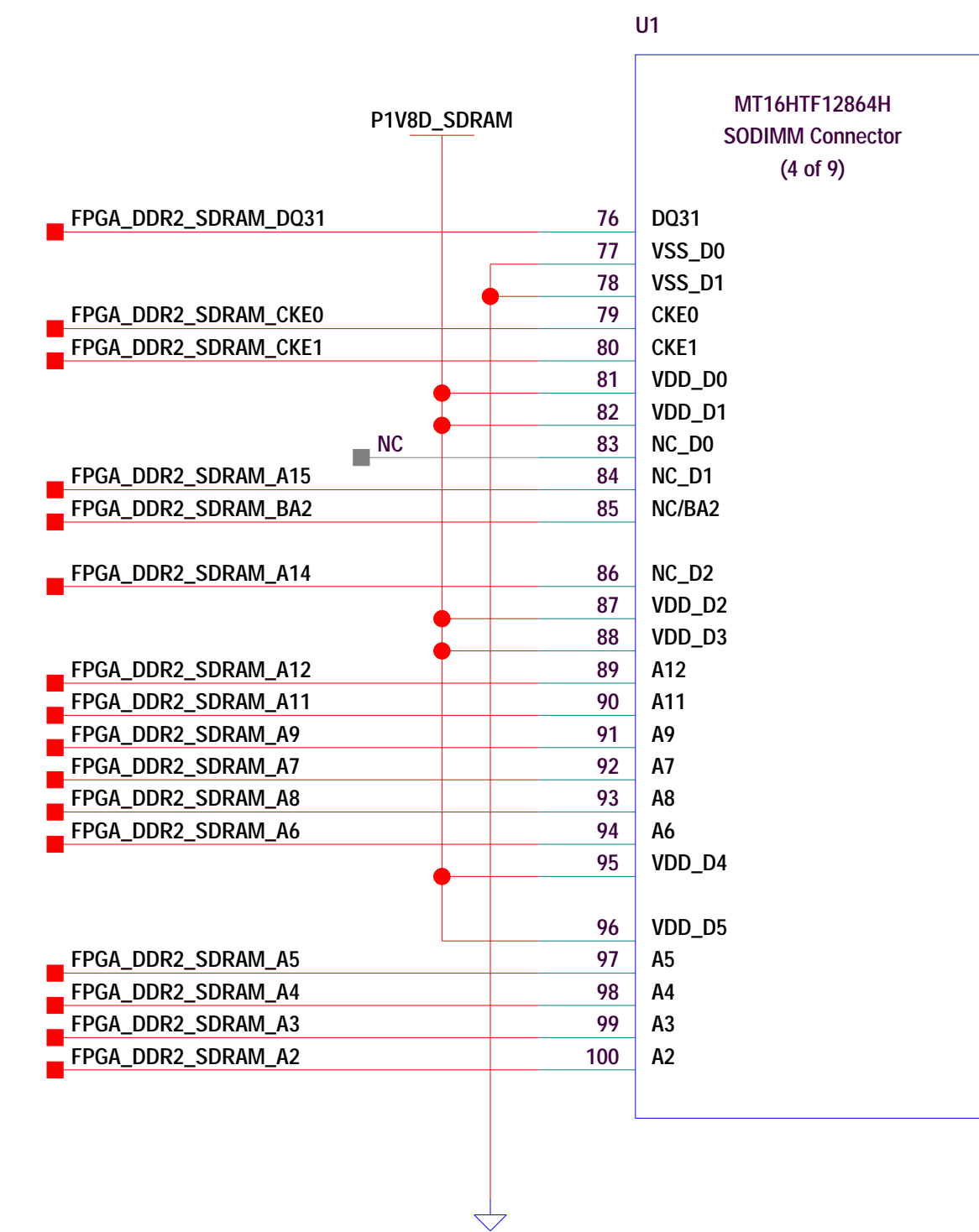
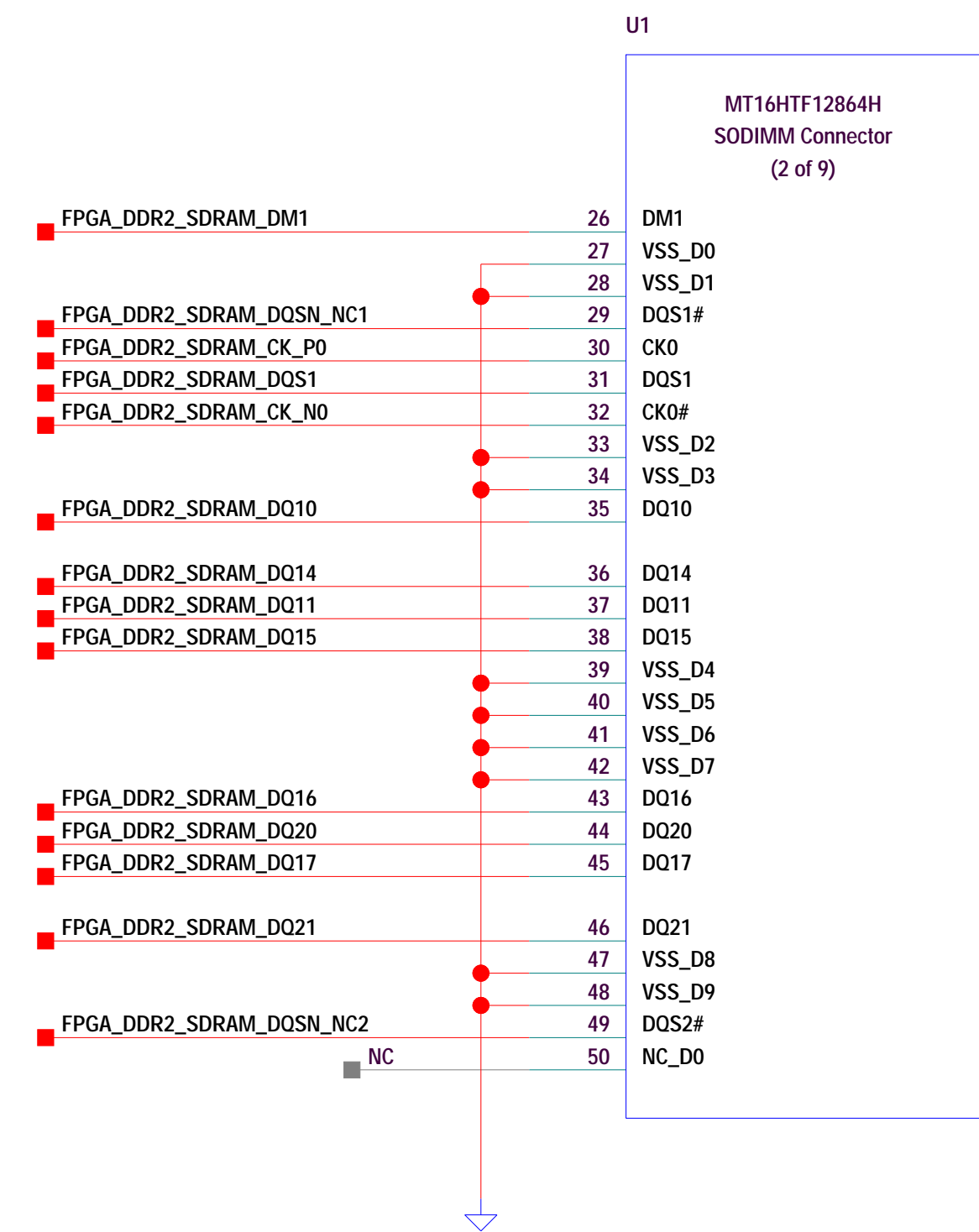
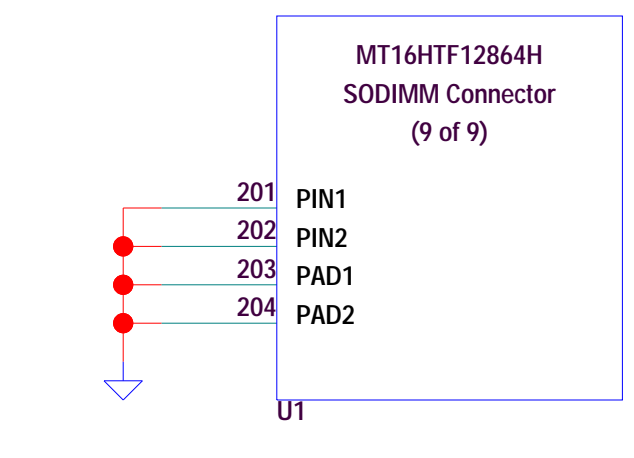
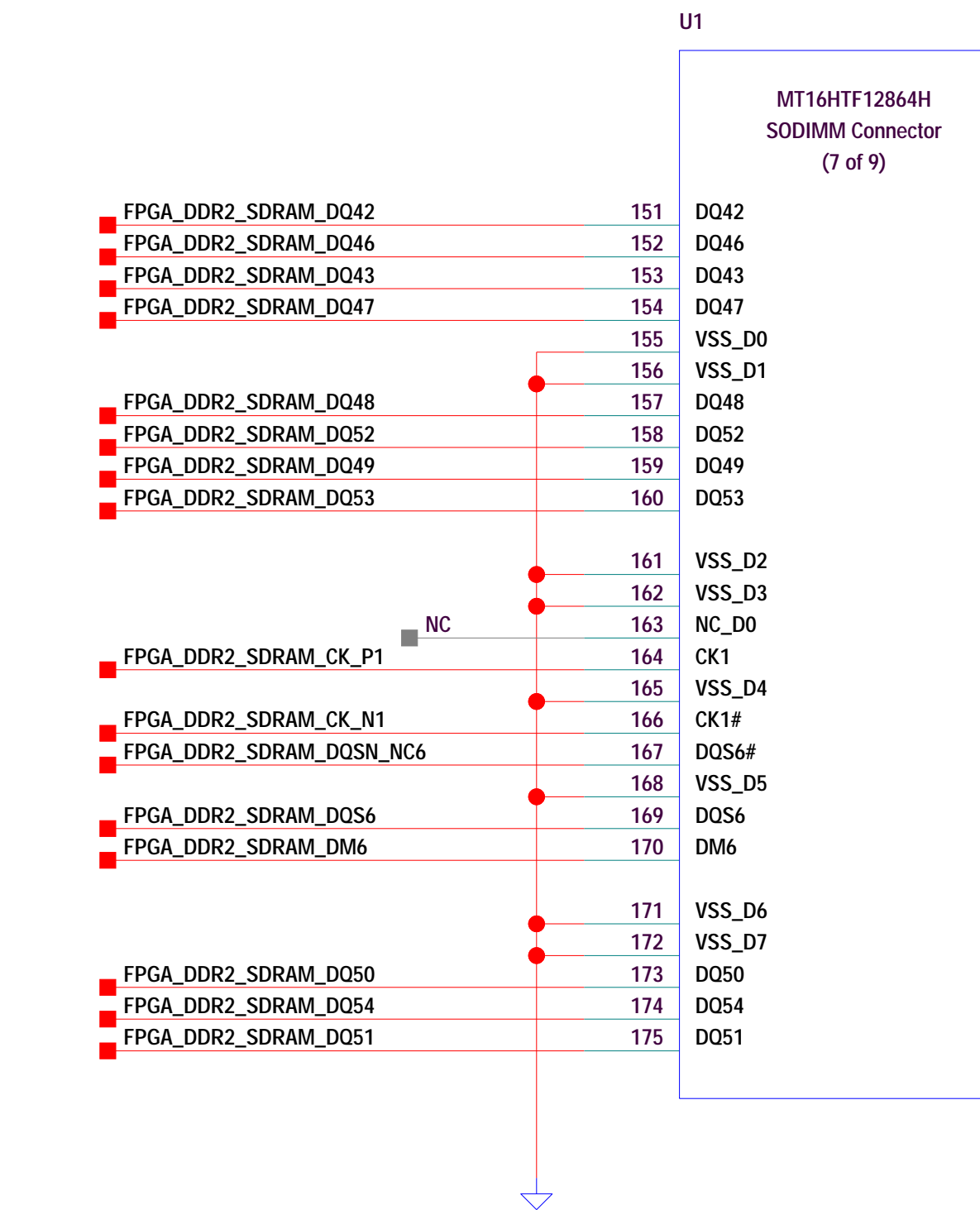
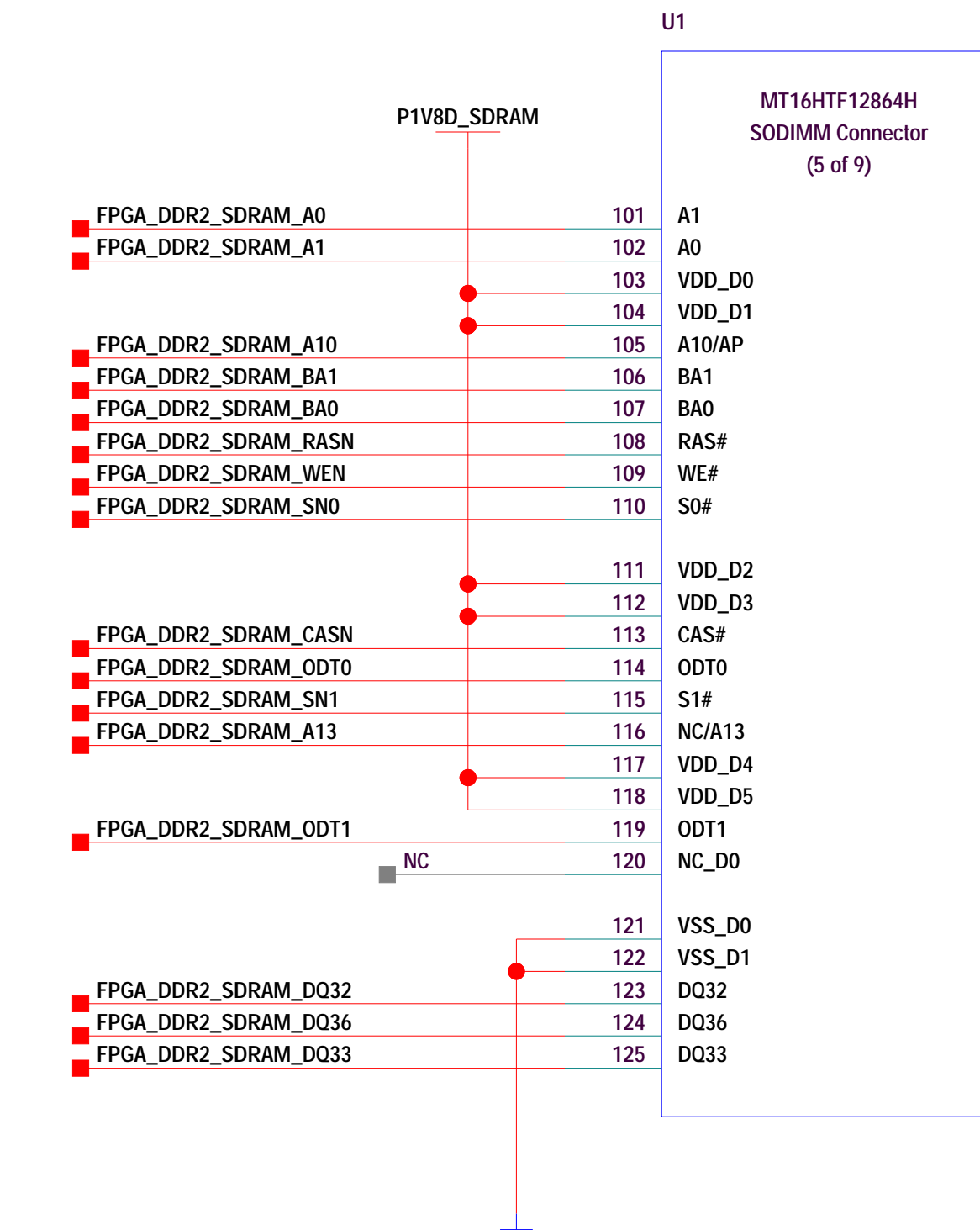
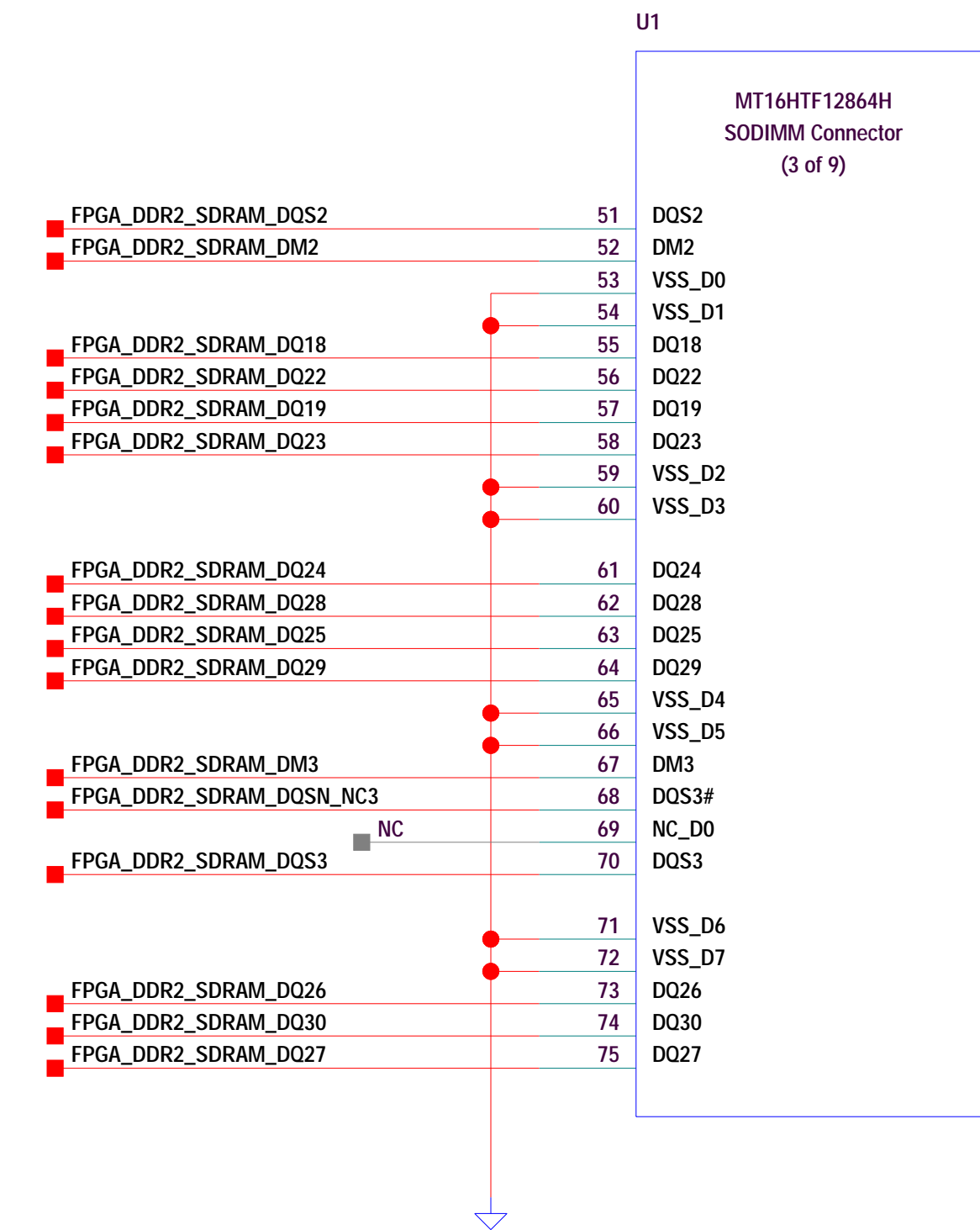
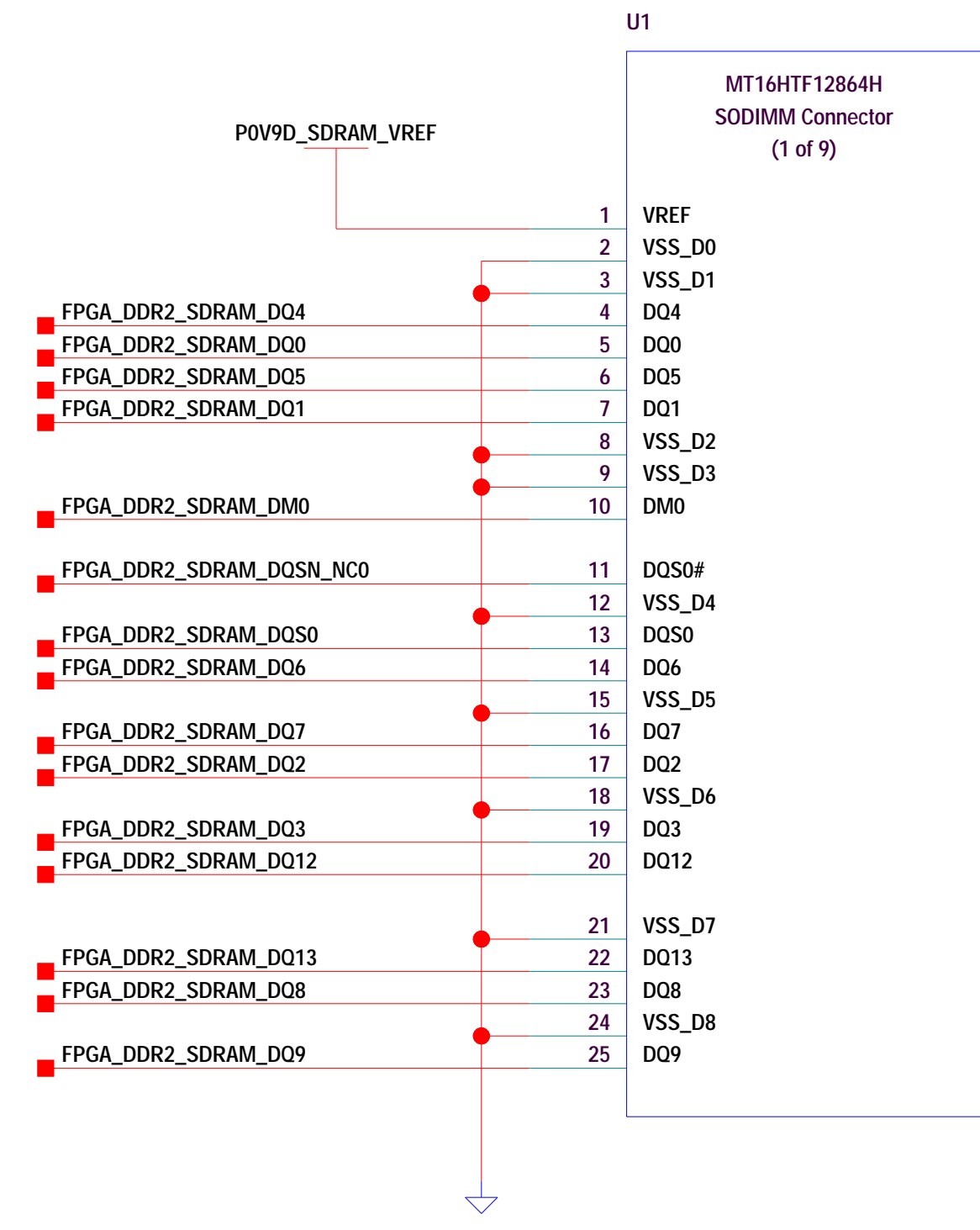
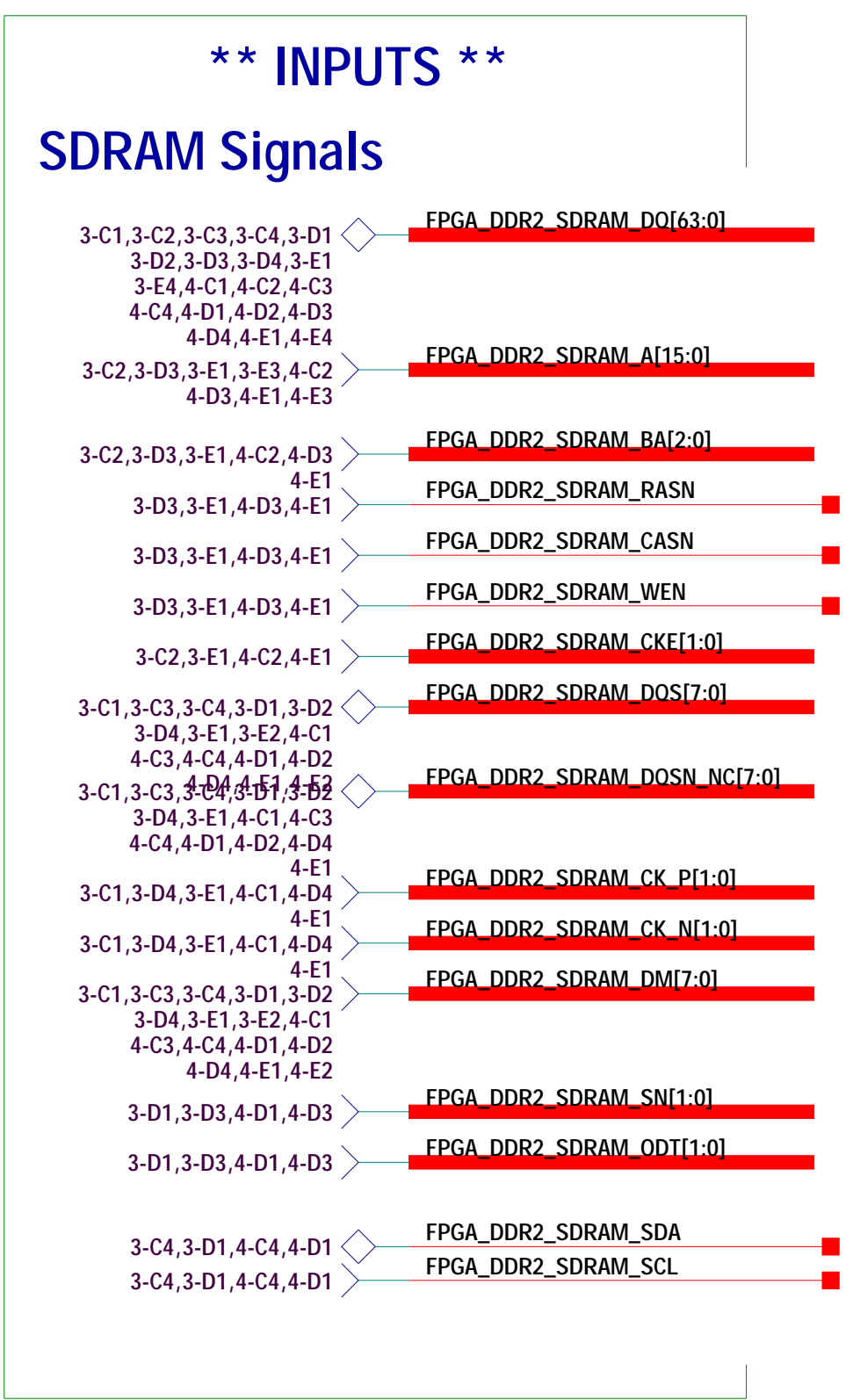


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Created by:	JEREMY W. WEBB	Date:	9-21-2009_8:29
Modified by:		Date:	
PCB NO:	389	Size:	B
	Sheet	2 of 4	REV: 3

DDR2 SDRAM SODIMM - MT16HTF25664H – 2GB (TOP - SIDE)

256 Meg x 64-bits

Provides between 1.024 GS and 1.280 GS storage of sampled data.



VLSI Computation LAB

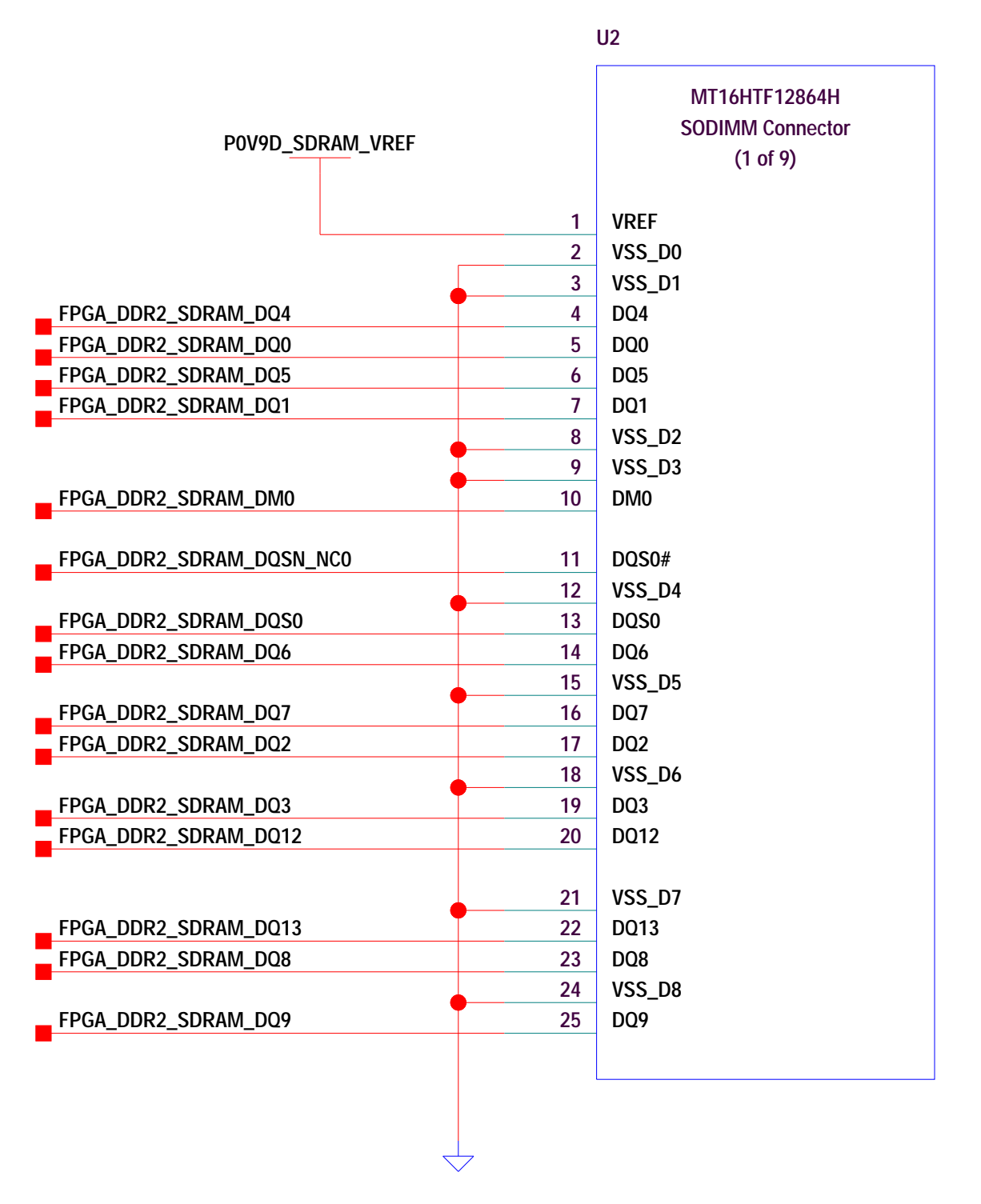
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 File: SODIMM_INTERPOSER
 Created by: JEREMY W. WEBB Date: 9-21-2009_8:29
 Modified by: Date:
 PCB NO: 389 Size: E Sheet 3 of 4 REV: 3

DDR2 SDRAM SODIMM - MT16HTF25664H – 2GB (BOTTOM - SIDE)

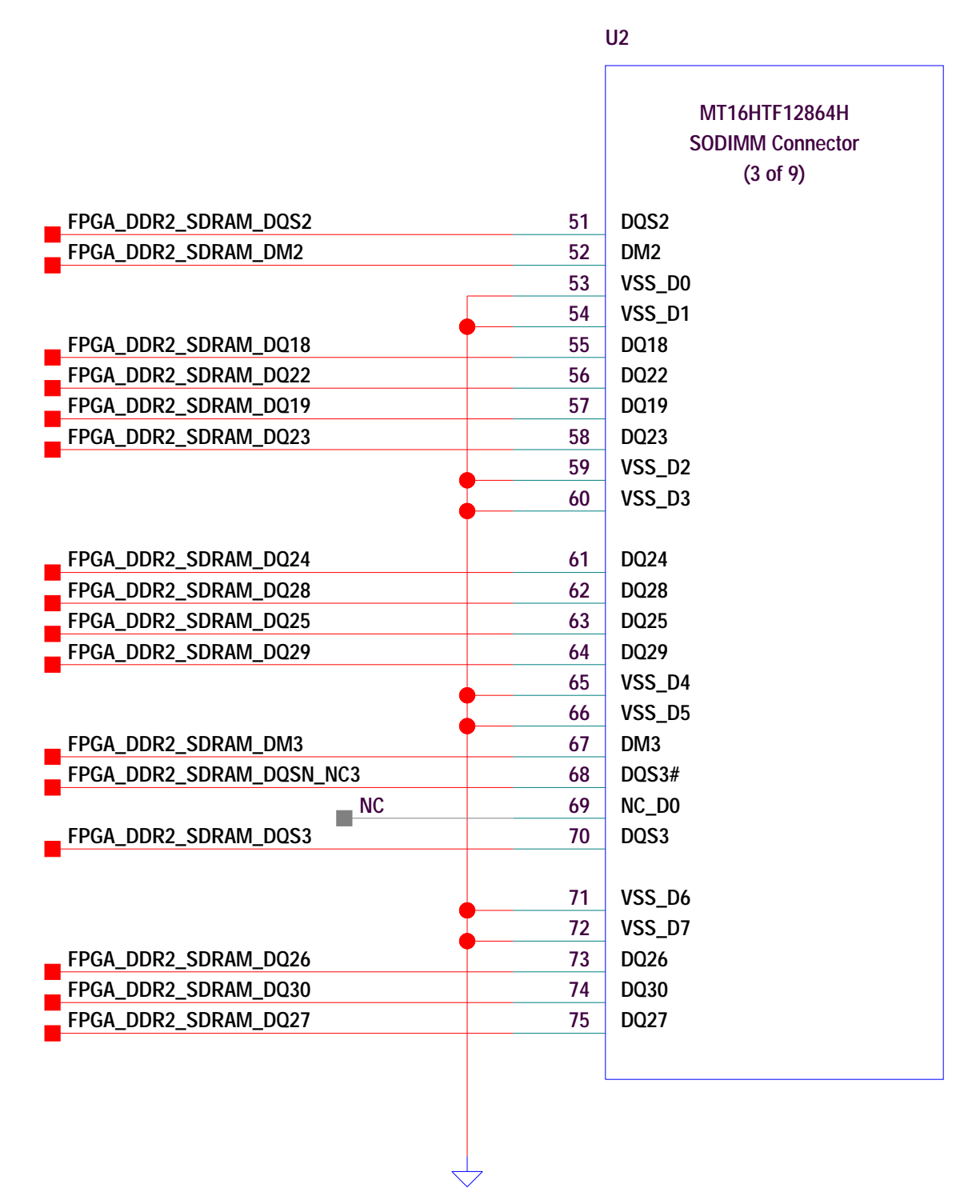
256 Meg x 64-bits

Provides between 1.024 GS and 1.280 GS storage of sampled data.

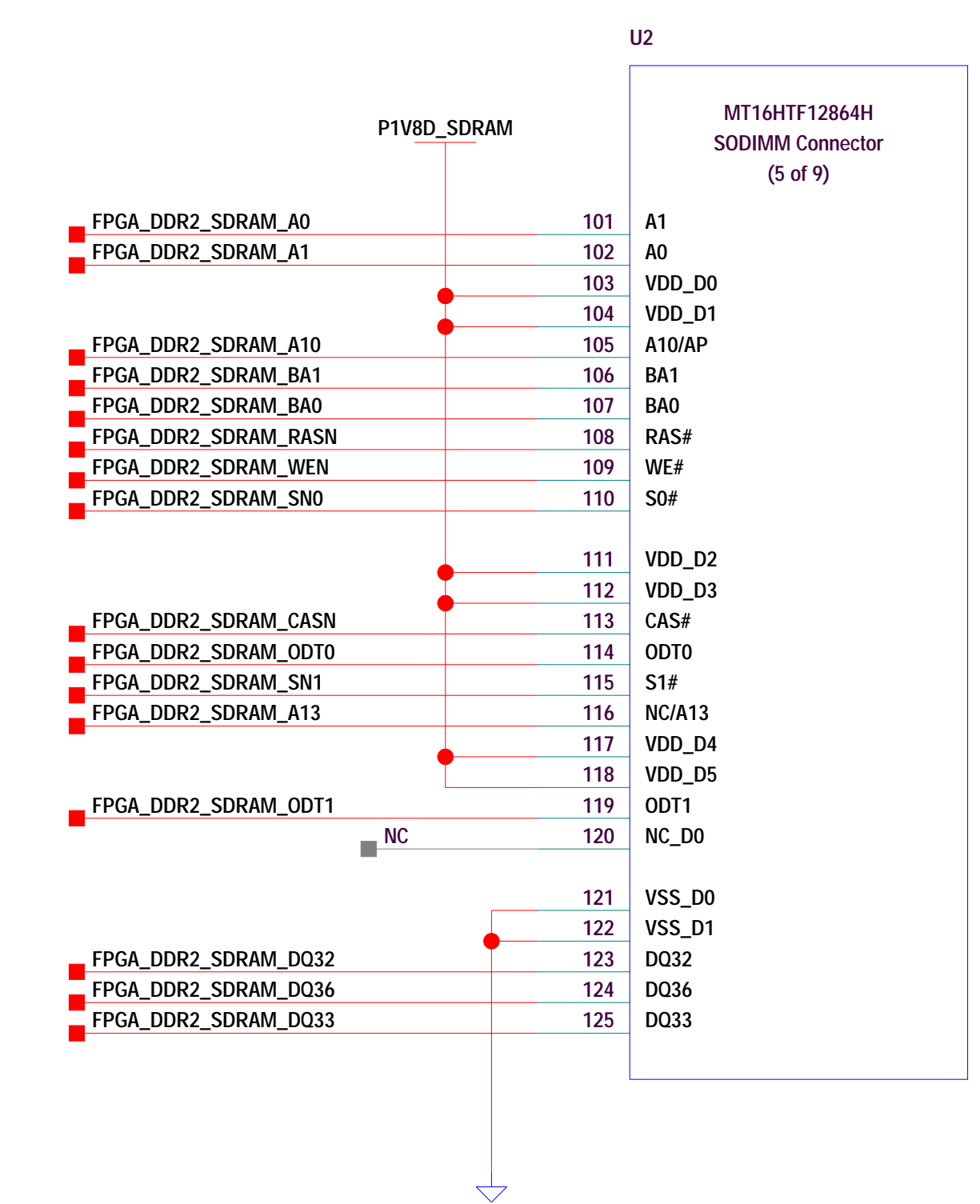
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- SDRAM Signals**
- FPGA_DDR2_SDRAM_DQ63[0]
 - FPGA_DDR2_SDRAM_A15[0]
 - FPGA_DDR2_SDRAM_BA[2:0]
 - FPGA_DDR2_SDRAM_RASN
 - FPGA_DDR2_SDRAM_CASN
 - FPGA_DDR2_SDRAM_WEN
 - FPGA_DDR2_SDRAM_CKE1[0]
 - FPGA_DDR2_SDRAM_DQS7[0]
 - FPGA_DDR2_SDRAM_DQS1[0]
 - FPGA_DDR2_SDRAM_DM17[0]
 - FPGA_DDR2_SDRAM_DM1[0]
 - FPGA_DDR2_SDRAM_S1[0]
 - FPGA_DDR2_SDRAM_S11[0]
 - FPGA_DDR2_SDRAM_SDA
 - FPGA_DDR2_SDRAM_SCL



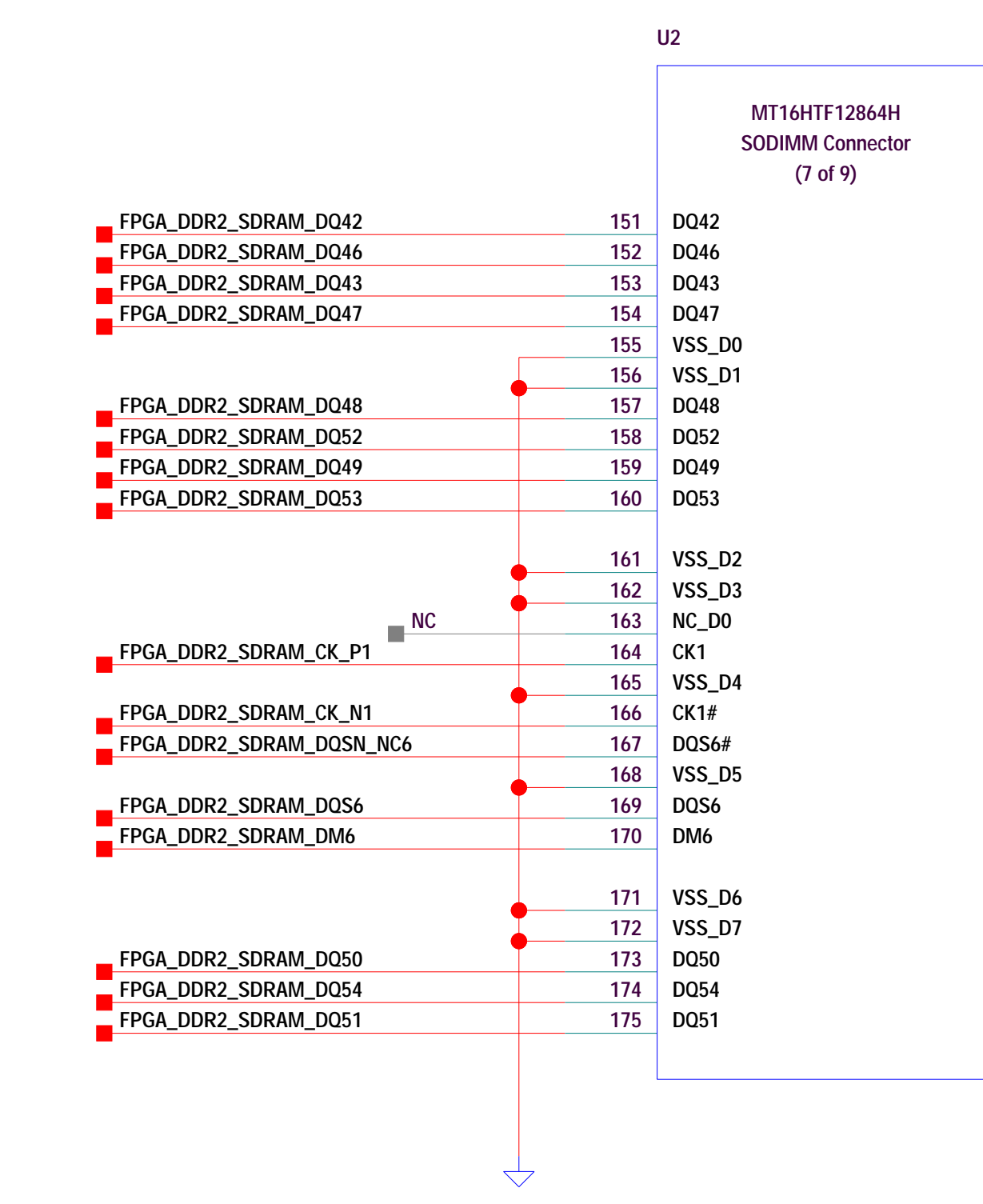
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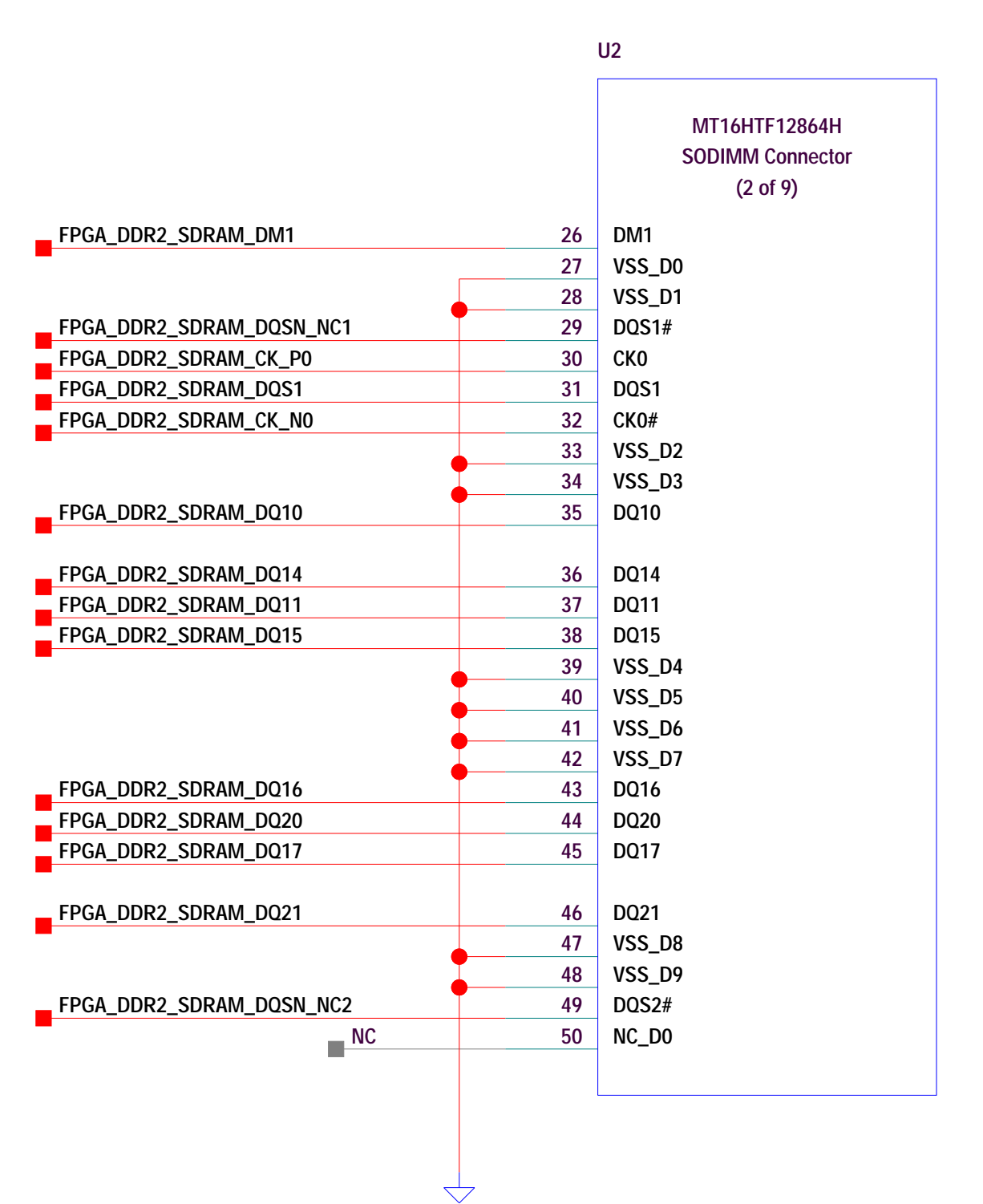
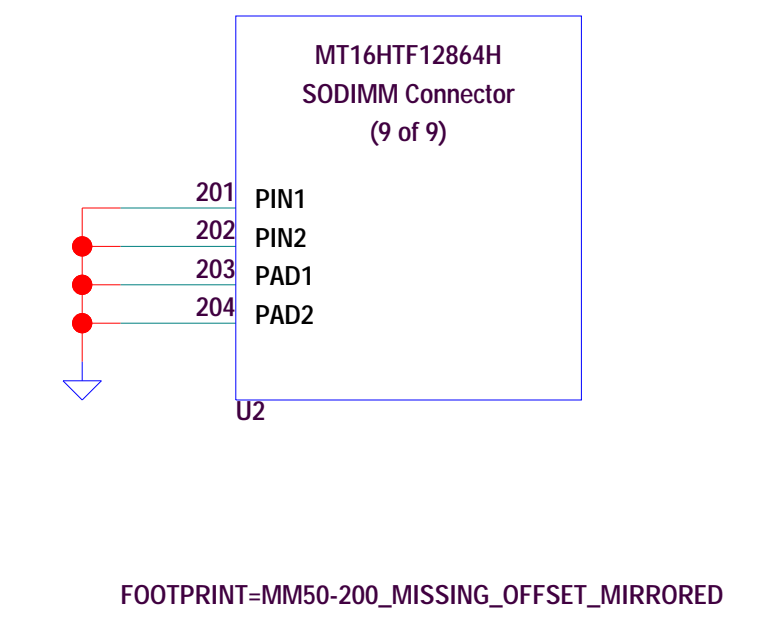
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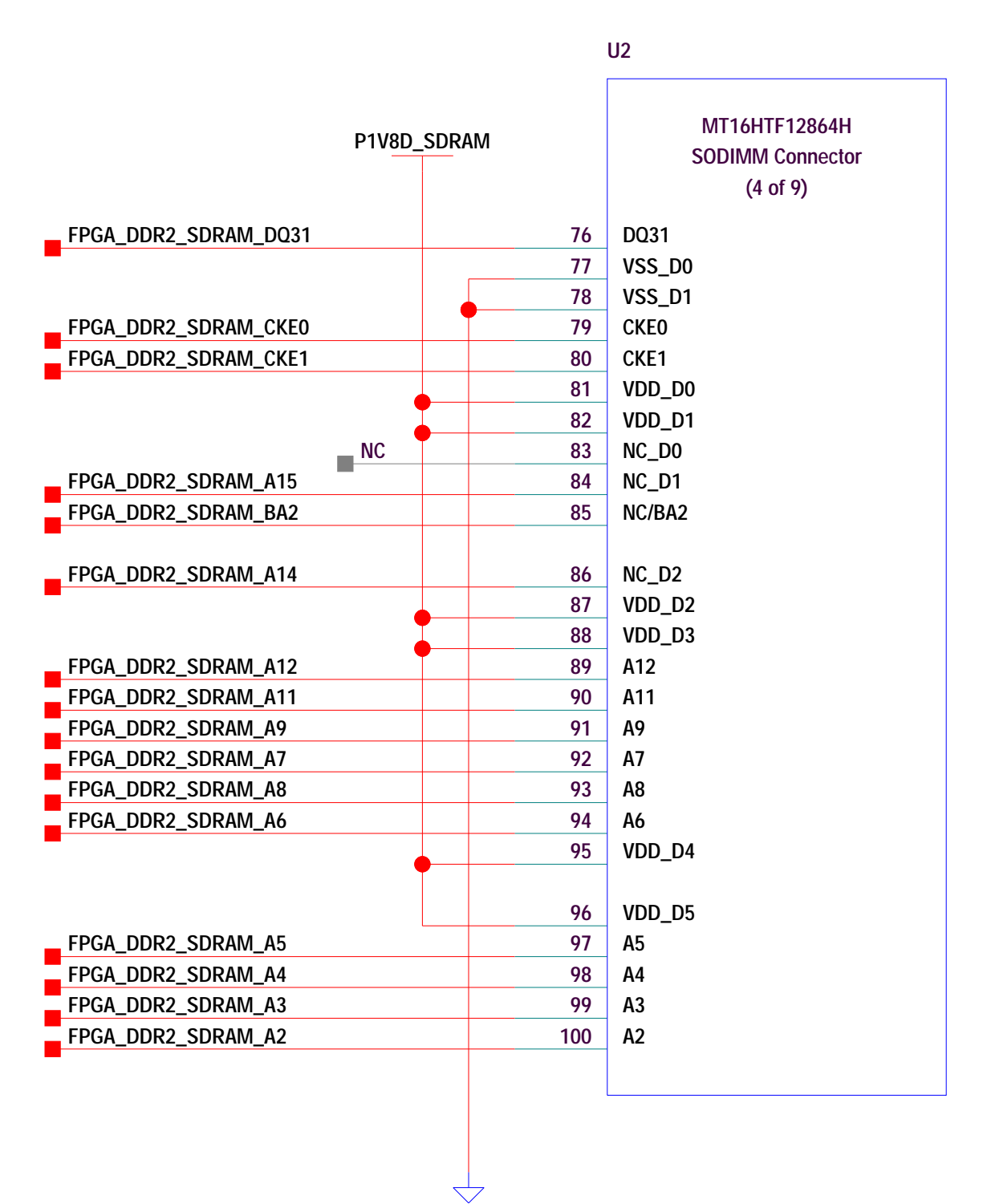
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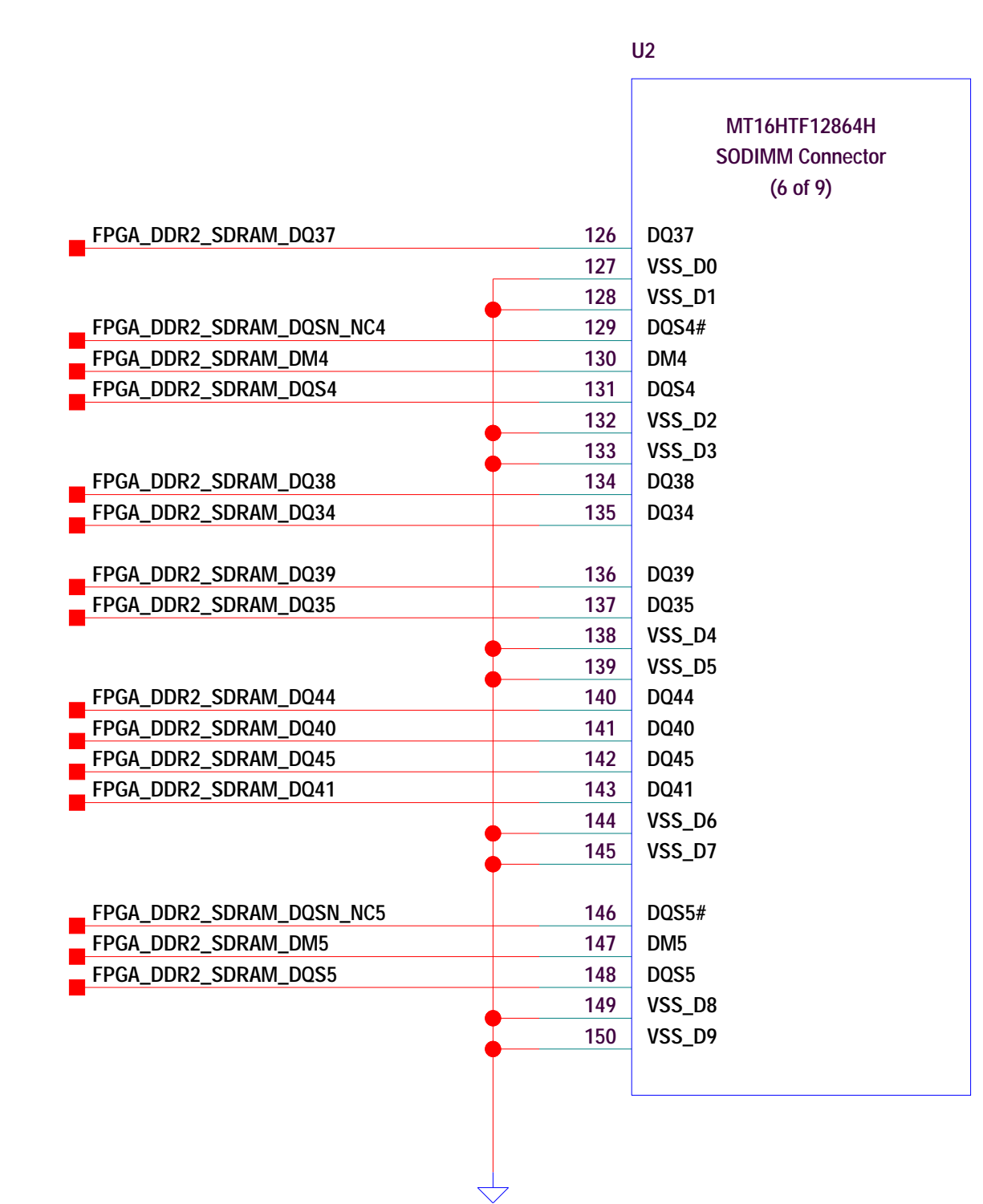
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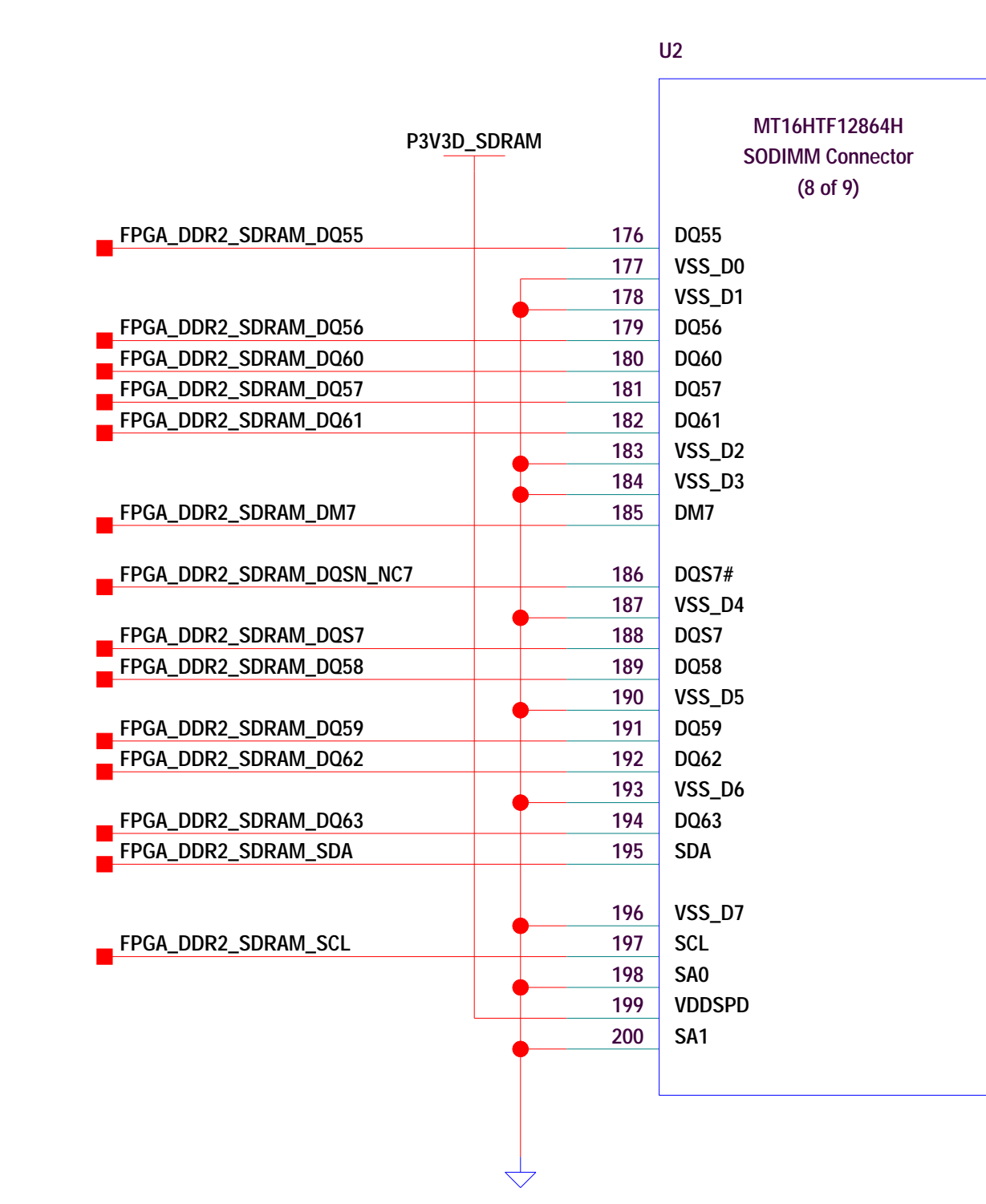
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FOOTPRINT=MM50-200_MISSING_OFFSET_MIRRORED



FOOTPRINT=MM50-200_MISSING_OFFSET_MIRRORED



FOOTPRINT=MM50-200_MISSING_OFFSET_MIRRORED

VLSI Computation LAB

Title: DDR2 SDRAM SODIMM - MT16HTF25664H – 2GB (BOTTOM - SIDE)
 File: SODIMM_INTERPOSER
 Created by: JEREMY W. WEBB Date: 9-21-2009_8:29
 Modified by: Date:
 PCB NO: 389 Size: E Sheet 4 of 4 REV: 3